

JUL 31 2006

REMARKS

Claims 1-2, 4-9, and 11-25 are pending. Claims 15-22 are rejected under 35 U.S.C. § 102(e). Claims 1-2, 4-9, 11-14, and 23-25 are rejected under 35 U.S.C. § 103(a). Claims 1, 11-12, 15-16, 20, 22, and 25 are currently amended. Claims 3 and 10 were previously cancelled without prejudice.

Claims 20 is amended as suggested by Examiner to more clearly define a particular embodiment the present invention.

Claims 15-22 are rejected under 35 U.S.C. § 102(e) as being anticipated by Stark et al. (U.S. Pat. No. 6,282,181). Claim 15 is amended to recite "A receiver, comprising: **a memory arranged to store a plurality of phase-shifting masks at a first time**; a memory port to supply the plurality of phase-shifting masks; an application means to determine a first time interval, the application means cross-referencing the first time interval to the plurality of phase-shifting masks, the application means having an output connected to the memory port to request the plurality of phase-shifting masks; and a pseudorandom noise (PN) code generator having a first input connected to the memory to accept the plurality of phase-shifting masks, the PN code generator offsetting a PN code with each phase-shifting mask of the plurality of phase-shifting masks, the PN code generator having an output to supply the PN code with a second phase, offset from the PN code first phase." (emphasis added).

Examiner has rejected depending claim 16, which also recites the memory of independent claim 15. Examiner identifies figures 6-9 and column 8, line 11 through column 10, line 23 of Stark et al. as an anticipatory disclosure for claim 16. Applicants respectfully disagree. Stark et al. disclose a shift register at Figure 4 which iteratively produces each sequence as needed. Operation of this shift register is explained at column 7, line 42 through column 8, line 35. Applicants fail to find any disclosure by Stark et al. of "a memory arranged to store a plurality of phase-shifting

TI-31693, Page 8

masks at a first time" as required by claims 15-22. Thus, applicants respectfully submit that claims 15-22 are patentable under 35 U.S.C. § 102(e) over Stark et al.

Claims 1-2, 4-9, 11-14, and 23-25 are rejected under 35 U.S.C. § 103(a) as being unpatentable over Easton et al. (U.S. Pat. No. 6,590,886) in view of Stark et al. Independent claim 1 is amended to recite "selecting a plurality of phase-shifting masks stored in a memory at a first time in response to the first time interval." Independent claim 25 is amended to recite "storing a plurality of phase-shifting masks in a memory at a first time." Examiner admits Easton et al. do not disclose selecting a plurality of phase-shifting masks and shifting the PN code first phase with each of the phase-shifting masks. Examiner relies on Stark et al. for these limitations. (Office Action 2/2/2006, page 7). As previously discussed with regard to claim 15, however, Stark et al. do not disclose "selecting a plurality of phase-shifting masks stored in a memory at a first time in response to the first time interval" as required by claims 1-2, 4-9, and 11-14. Furthermore, Stark et al. do not disclose "storing a plurality of phase-shifting masks in a memory at a first time" as required by claim 25. Thus, applicants further submit that claims 1-2, 4-9, 11-14, and 23-25 are patentable under 35 U.S.C. § 103(a) over Easton et al. in view of Stark et al.

In view of the foregoing, applicants respectfully request reconsideration and allowance of claims 1-2, 4-9, and 11-25. If the Examiner finds any issue that is unresolved, please call applicants' attorney by dialing the telephone number printed below.

Respectfully submitted,



Robert N. Rountree
Attorney for Applicants
Reg. No. 39,347

Robert N. Rountree, LLC
70360 Highway 69
Cotopaxi, CO 81223
Phone/Fax: (719) 783-0990

TI-31693, Page 9